IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: 2818

Examiner: E. Taylor

Applicant(s): Toshihide TSUBATA et al.

Application No.: 10/595,640

Confirmation No.: 3891

Filing or 371(c) Date: May 2, 2006

Title: TRANSISTOR AND CVD APPARATUS

USED TO DEPOSIT GATE INSULATING FILM THEREOF

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement issued on June 27, 2007, Applicant elects Group I, including Claims 1-5 and 8 for prosecution in the present application. This election is made without traverse.

Applicant hereby reserves the right to file a Divisional Application to pursue prosecution of the non-elected claims 6 and 7.

Applicant respectfully submits that this application is in condition for allowance.

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Favorable consideration and prompt allowance of the present application are respectfully requested.

Respectfully submitted,

Registration No. 37,368

Dated: July 13, 2007

/Stephen R. Funk #57,751/
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